

09-12-05

Atty. Dkt. No. 039153-0684 (H1719)

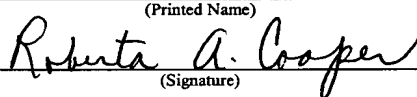
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Xiang et al.

Title: METHOD OF
FABRICATING AN
INTEGRATED CIRCUIT
CHANNEL REGION

(formerly "A METHOD OF
FABRICATING A
STRAINED SILICON
CHANNEL FINFET")

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 593168852 US	09/09/05
(Express Mail Label Number)	(Date of Deposit)
Roberta A. Cooper	
(Printed Name)	
	
(Signature)	

Appl. No.: 10/755,763

Filing Date: 01/12/2004

Examiner: James M. Mitchell

Art Unit: 2813

Confirmation No.: 9627

AMENDMENT TRANSMITTAL

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an amendment in the above-identified application.

[X] Amendment and Reply Under 37 CFR 1.111 (9 pages).

[X] The fee required for additional claims is calculated below:

	Claims As Amended		Previously Paid For		Extra Claims Present		Rate		Additional Claims Fee
Total Claims:	20	-	20	=	0	x	\$50.00	=	\$0.00
Independent Claims:	3	-	3	=	0	x	\$200.00	=	\$0.00
First presentation of any Multiple Dependent Claims:		+					\$360.00	=	\$0.00
							CLAIMS FEE TOTAL	=	\$0.00

☐ Applicant hereby petitions for an extension of time under 37 C.F.R. §1.136(a) for the total number of months checked below:

<input type="checkbox"/> Extension for response filed within the first month:	\$120.00	\$0.00
<input type="checkbox"/> Extension for response filed within the second month:	\$450.00	\$0.00
<input type="checkbox"/> Extension for response filed within the third month:	\$1,020.00	\$0.00
<input type="checkbox"/> Extension for response filed within the fourth month:	\$1,590.00	\$0.00
<input type="checkbox"/> Extension for response filed within the fifth month:	\$2,160.00	\$0.00
EXTENSION FEE TOTAL:		\$0.00
<input type="checkbox"/> Statutory Disclaimer Fee under 37 C.F.R. 1.20(d):	\$130.00	\$0.00
CLAIMS, EXTENSION AND DISCLAIMER FEE TOTAL:		\$0.00
<input type="checkbox"/> Small Entity Fees Apply (subtract ½ of above):		\$0.00
TOTAL FEE:		\$0.00

☒ The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 06-1447. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 06-1447. If any extensions of time are needed for timely acceptance of papers submitted herewith, applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extensions fees to Deposit Account No. 06-1447.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date

9/9/05

By



Marcus W. Sprow
Attorney for Applicant
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(Express Mail Label Number)	(Date of Deposit)
<u>Roberta A. Cooper</u>	
(Printed Name)	
<u>Roberta A. Cooper</u>	
(Signature)	

AMENDMENT AND REPLY UNDER 37 CFR 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This communication is responsive to the Notice of Non-Compliant Amendment dated August 26, 2005 and the Non-Final Office Action dated July 29, 2005, concerning the above-referenced patent application.

Amendments to the Title begin on page 2 of this document.

Amendments to the Specification begin on page 3 of this document.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this document.

Remarks/Arguments begin on page 7 of this document.

Please amend the application as follows:

Amendments to the Title:

Please amend Title of the present application as follows:

~~A Method Of Fabricating A Strained Silicon Channel Finfet~~ Method of Fabricating an Integrated Circuit Channel Region